

<b>L Number</b>	<b>Hits</b>	<b>Search Text</b>	<b>DB</b>	<b>Time stamp</b>
<b>1</b>	<b>2</b>	<b>p adj spice same evaluat\$4</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB</b>	<b>2004/10/25 16:38</b>
<b>2</b>	<b>0</b>	<b>p adj spice same test same circuit</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB</b>	<b>2004/10/25 16:38</b>
<b>3</b>	<b>3439</b>	<b>circuit adj design same (test evaluat\$4)</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB</b>	<b>2004/10/25 17:13</b>
<b>4</b>	<b>6</b>	<b>circuit adj design same test adj evaluation</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB</b>	<b>2004/10/25 17:08</b>
<b>5</b>	<b>14</b>	<b>5682336.pn,uref,repn,rlpn,bi.</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB</b>	<b>2004/10/25 17:09</b>
<b>6</b>	<b>32</b>	<b>(circuit adj design same (test evaluat\$4)) same spice</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB</b>	<b>2004/10/25 17:13</b>
<b>7</b>	<b>20</b>	<b>((circuit adj design same (test evaluat\$4)) same spice) and first same second same (test evaluat\$4 simulat\$4)</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB</b>	<b>2004/10/25 17:14</b>